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transistor Q1 rise and, so long as output power remains low, transistor Q1 remains off. -

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A generator of a scanning velocity modulation deflection signal generator, comprising:

a variable conduction device comprising a transistor (Q1) coupled to said generator and having a first input (Q1e) responsive to a negative feedback signal scanning velocity modulation deflection signal (Vm), and a second input (Q1b) responsive to a control signal;

in a first condition said ~~device transistor (Q1)~~ providing a feedback path (Q1e-Q1e) for said negative feedback signal for controlling said ~~[[a]]~~ scanning velocity modulation deflection signal (Vm) in magnitude; and,

in a second condition said ~~device transistor (Q1)~~ interrupting said feedback path (Q1e-Q1e) and substantially inhibiting generation of said scanning velocity modulation deflection signal (Vm).

2. (Currently Amended) The ~~scanning velocity modulation deflection signal generator of claim 1, wherein during said first condition said variable conduction device transistor (Q1) varies conduction in accordance with said a magnitude of said scanning velocity modulating deflection signal (Vm) negative feedback signal.~~

3. (Currently Amended) The ~~scanning velocity modulation deflection signal generator of claim 2, wherein said variable conduction device transistor (Q1) varies conduction to variably attenuate a said scanning velocity modulating signal (SVM) in accordance with said scanning velocity modulating deflection signal (Vm) said negative feedback signal magnitude.~~

4. (Currently Amended) The ~~scanning velocity modulation deflection signal generator of claim 1, wherein during said second condition said variable conduction~~

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~~device transistor (Q1)~~ is fully conductive responsive to said control signal for substantially inhibiting said scanning velocity modulation deflection signal (V_m).

5. (Currently Amended) The ~~scanning velocity modulation deflection signal~~ generator of claim 1, wherein during said second condition said ~~variable conduction~~ device transistor (Q1) is fully conductive, attenuating said negative feedback signal ~~a scanning velocity modulating signal (SVM)~~ and substantially inhibiting generation of said scanning velocity modulation deflection signal (V_m) said.

6. (Currently Amended) The ~~scanning velocity modulation deflection signal~~ generator of claim 1, wherein said second condition conduction in said ~~variable~~ ~~conduction device transistor (Q1)~~ is unresponsive to ~~[[ef]]~~ said negative feedback signal ~~scanning velocity modulating deflection signal (V_m)~~.

7. (Canceled)

-- 8. (New) A generator of a scanning velocity modulation deflection signal, comprising:

a variable conduction device consisting of a single transistor, said transistor having a first terminal responsive to a scanning velocity modulation feedback signal, and a second terminal responsive to a control signal;

in a first condition said transistor providing a feedback path for said feedback signal for controlling a magnitude of said scanning velocity modulation deflection signal; and,

in a second condition said transistor interrupting said feedback path and substantially inhibiting generation of said scanning velocity modulation deflection signal. --

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-- 9. (New) A scanning velocity modulation deflection signal generator, comprising:

a transistor coupled to said scanning velocity modulation deflection signal generator and operational as a common base amplifier for a feedback signal coupled for controlling a magnitude of said scanning velocity modulation deflection signal, and said transistor being operational as a common emitter amplifier for interrupting said feedback signal and substantially inhibiting generation of said scanning velocity modulation deflection signal. --

--10. (New) The generator of claim 1, wherein said negative feedback signal is representative of power dissipation in a scanning velocity modulation drive amplifier responsive to said scanning velocity modulation deflection signal. --

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--11. (New) The scanning velocity modulation deflection signal generator of claim 8, wherein said second condition a signal input to said generator is attenuated to substantially inhibit said scanning velocity modulation deflection signal generation. -

--12. (New) The scanning velocity modulation deflection signal generator of claim 9, wherein said negative feedback signal is representative of power dissipation in a scanning velocity modulation drive amplifier responsive to said scanning velocity modulation deflection signal. --

--13. (New) The scanning velocity modulation deflection signal generator of claim 9, wherein said common emitter amplifier substantially inhibits generation of said scanning velocity modulation deflection signal by attenuating a signal input to said generator. --
